

FIG. 1A

PRIOR ART

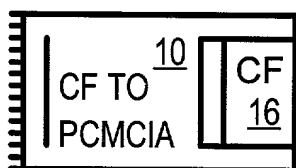


FIG. 1B

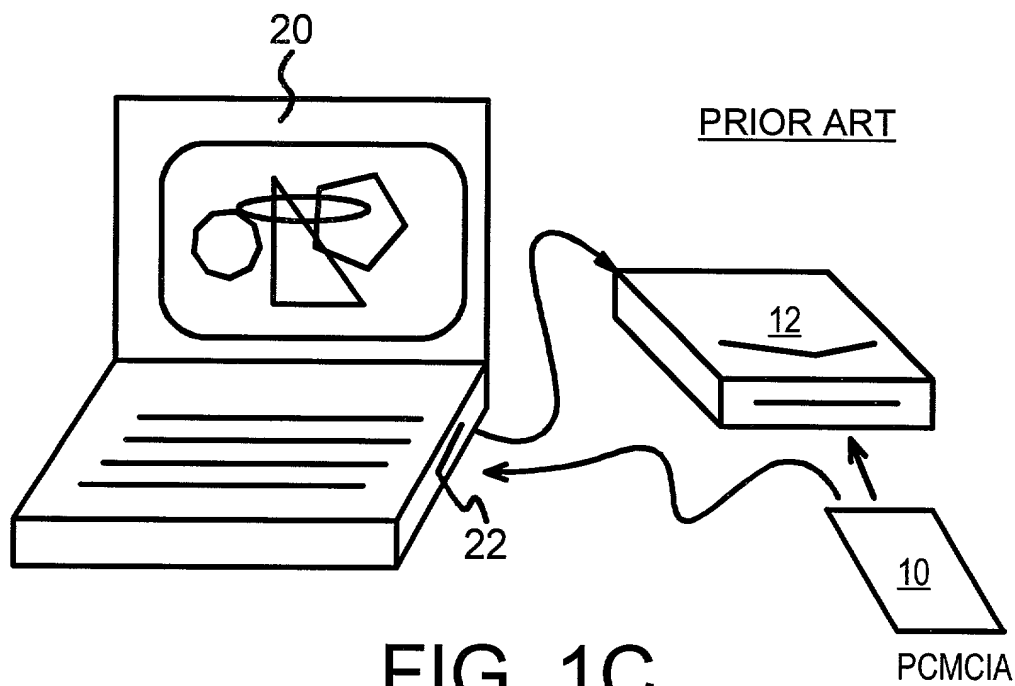
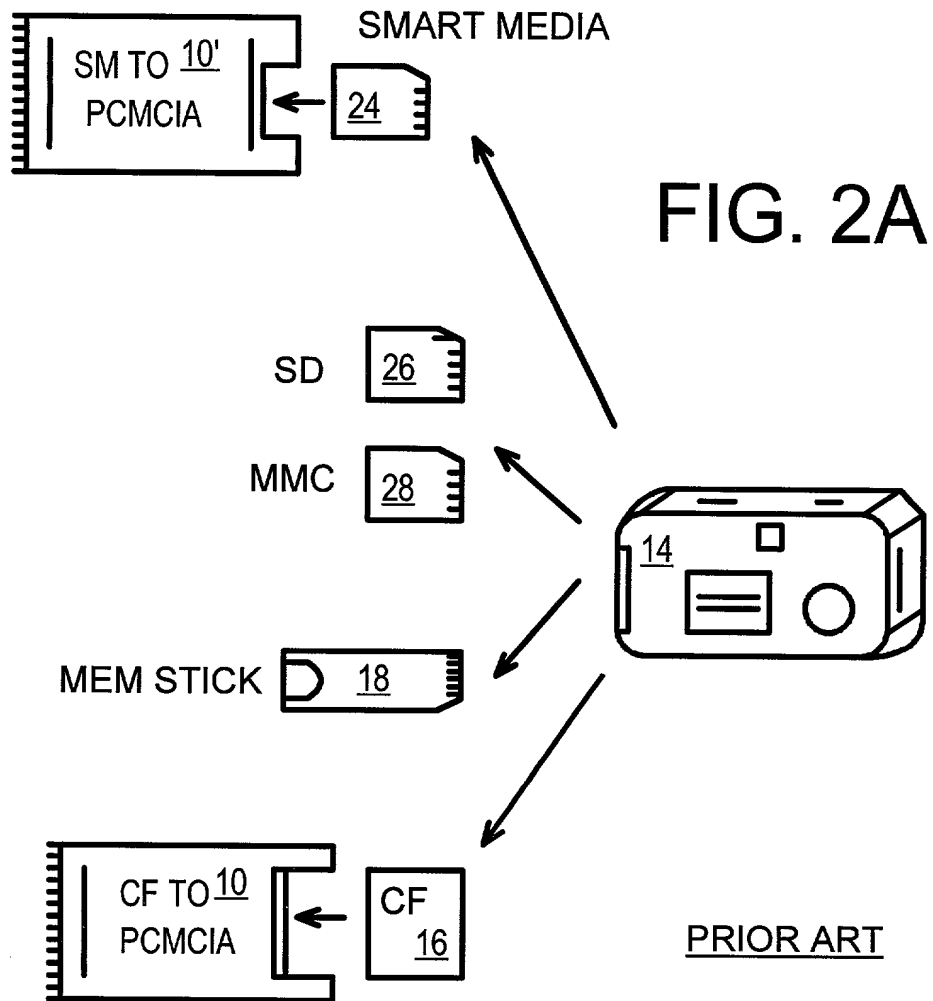


FIG. 1C

PRIOR ART



MEM STK TO PCMCIA

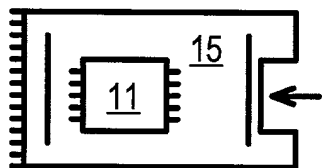


FIG. 2B

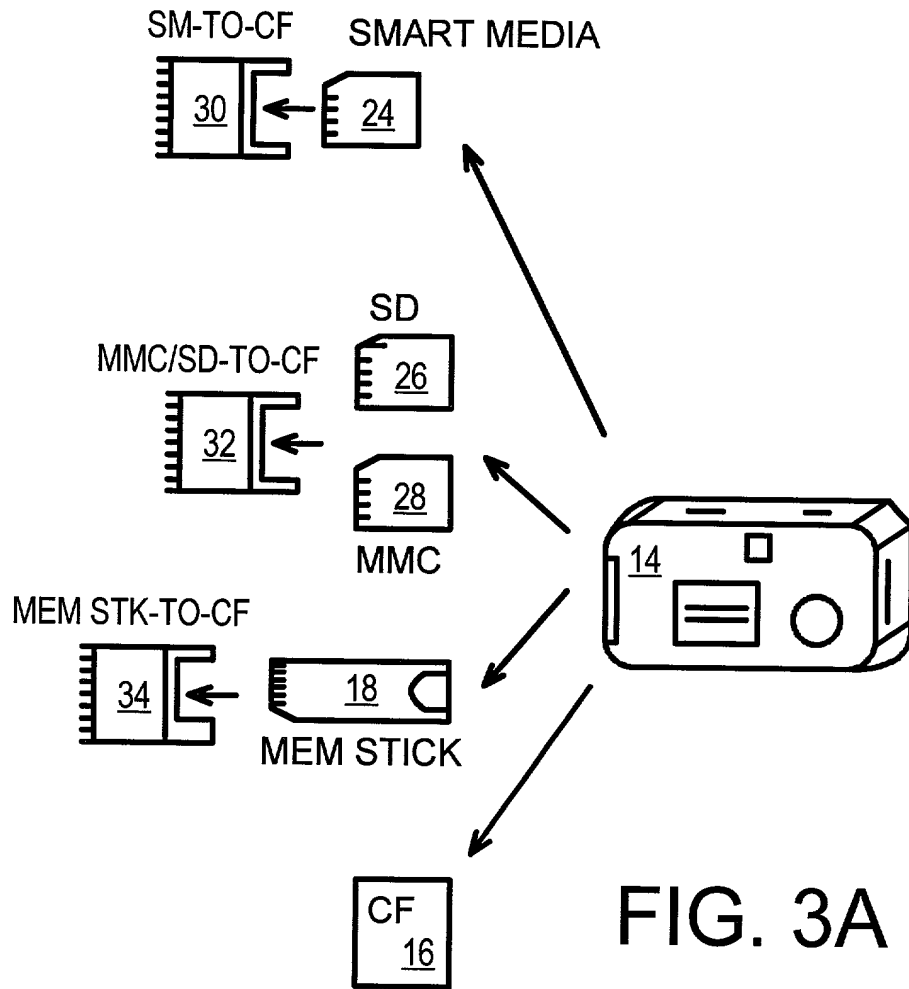


FIG. 3A

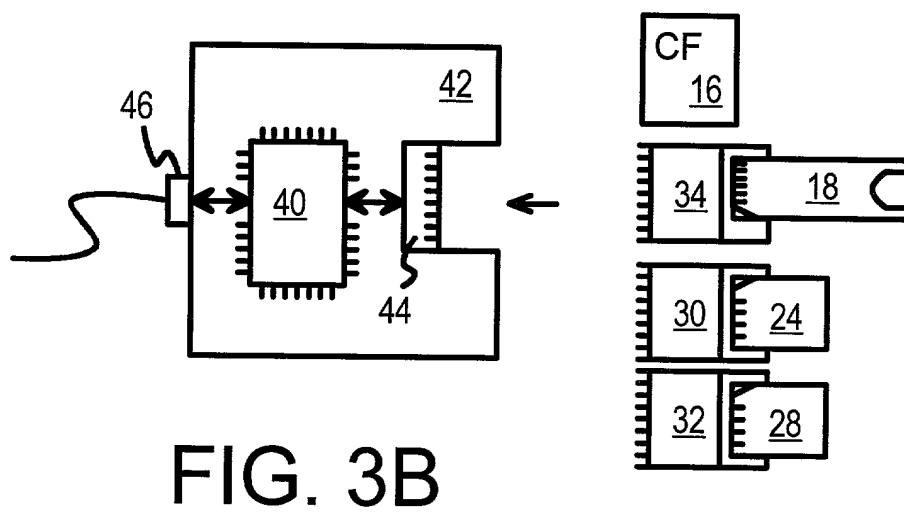


FIG. 3B

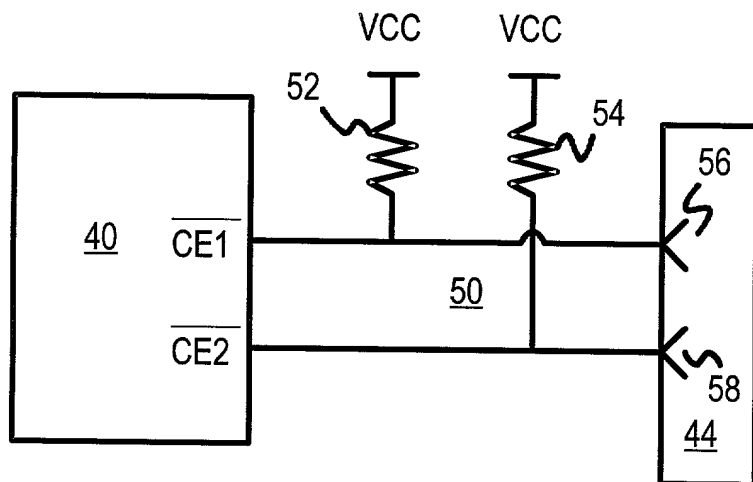
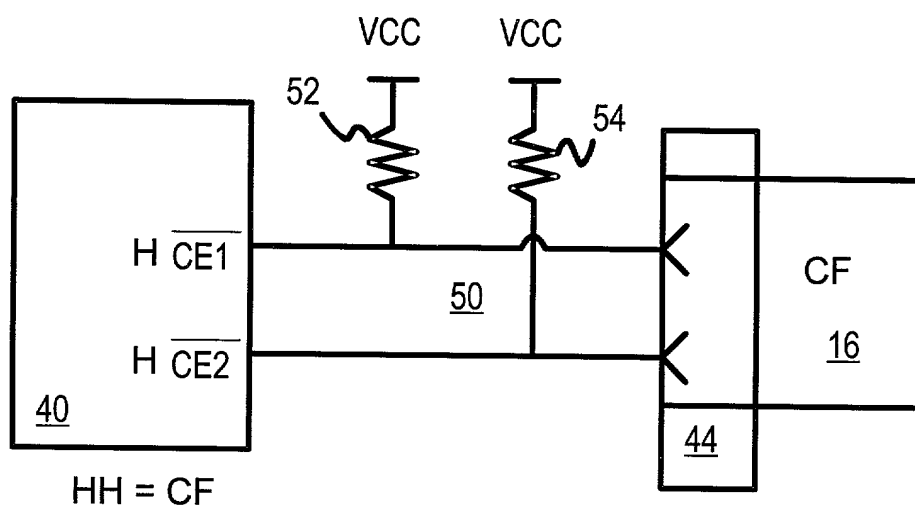
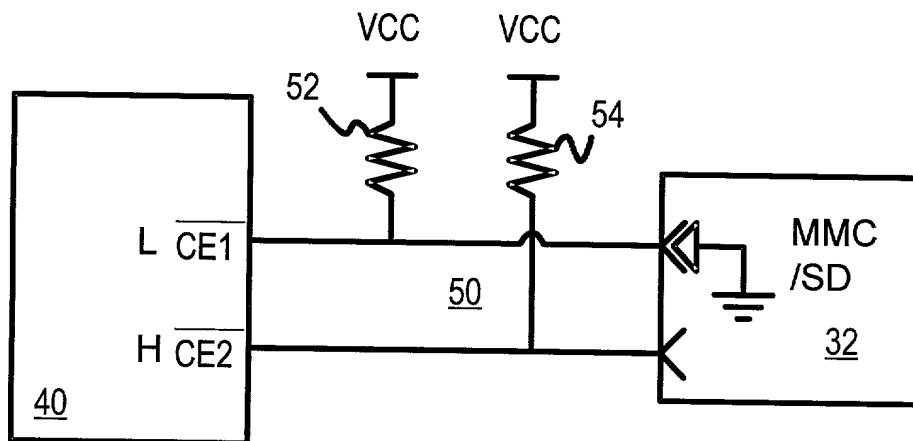


FIG. 4A



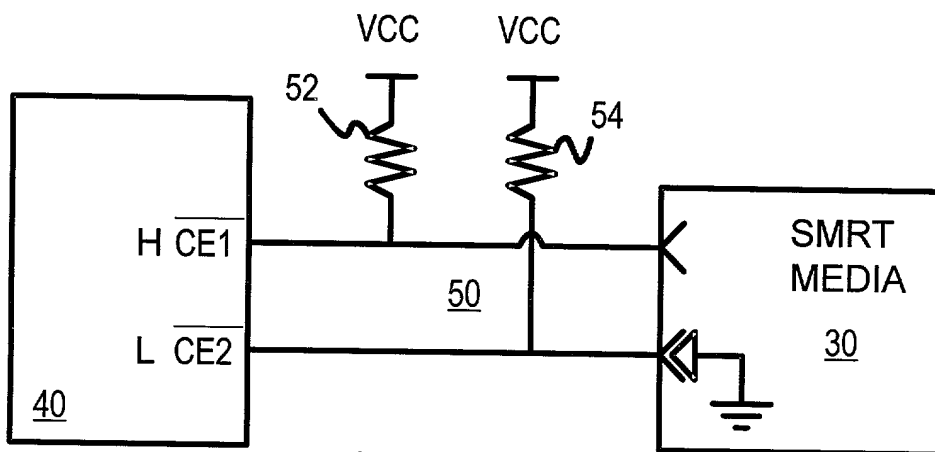
HH = CF

FIG. 4B



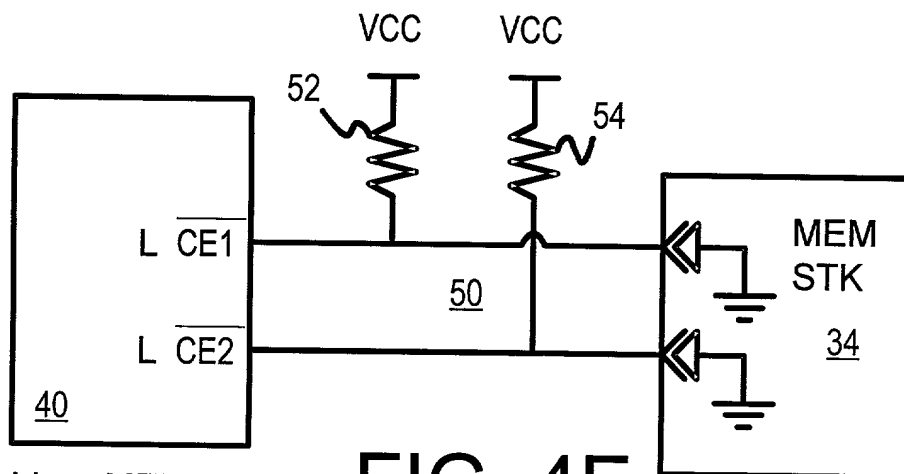
LH = MMC/SD

FIG. 4C



HL = SMRT MEDIA

FIG. 4D



LL = MEM STK

FIG. 4E

Pin	CF	Smart Media	MMC/SD	Memory Stick
1	Ground	Ground	Ground	Ground
2	D3	D3	---	---
3	D4	D4	---	---
4	D5	D5	---	---
5	D6	D6	---	---
6	D7	D7	---	---
7	-CE1	-SMCS	---	---
8	A10	---	---	---
9	-OE	-OE	---	---
10	A9	---	---	---
11	A8	---	---	---
12	A7	---	---	---
13	Power	Power	Power	Power
14	A6	CLE		
15	A5	ALE		
16	A4	READY		
17	A3	-WP		
18	A2	LVD	SERCLK	SERCLK
19	A1		DATAIO	DATAIO
20	A0		CMD	BITSET
21	D0	D0		
22	D1	D1		
23	D2	D2		
24	---	---	---	---
25	-CD2	-CD2	-CD2	-CD2
26	-CD1	-CD1	-CD1	-CD1
27	D11	---	---	---
28	D12	---	---	---
29	D13	---	---	---
30	D14	---	---	---
31	D15	---	---	---
32	-CE2	---	---	---
33	---	---	---	---
34	tie high	---	---	---
35	tie high	---	---	---
36	-WE	-WE	---	---
37	INTRQ	---	---	---
38	Power	Power	Power	Power
39	---	---	---	---
40	---	---	---	---
41	RESET	---	---	---
42	---	---	---	---
43	---	---	---	---
44	-REG	---	---	---
45	---	---	---	---
46	---	---	---	---
47	D8	---	---	---
48	D9	---	---	---
49	D10	---	---	---
50	Ground	Ground	Ground	Ground

FIG. 5

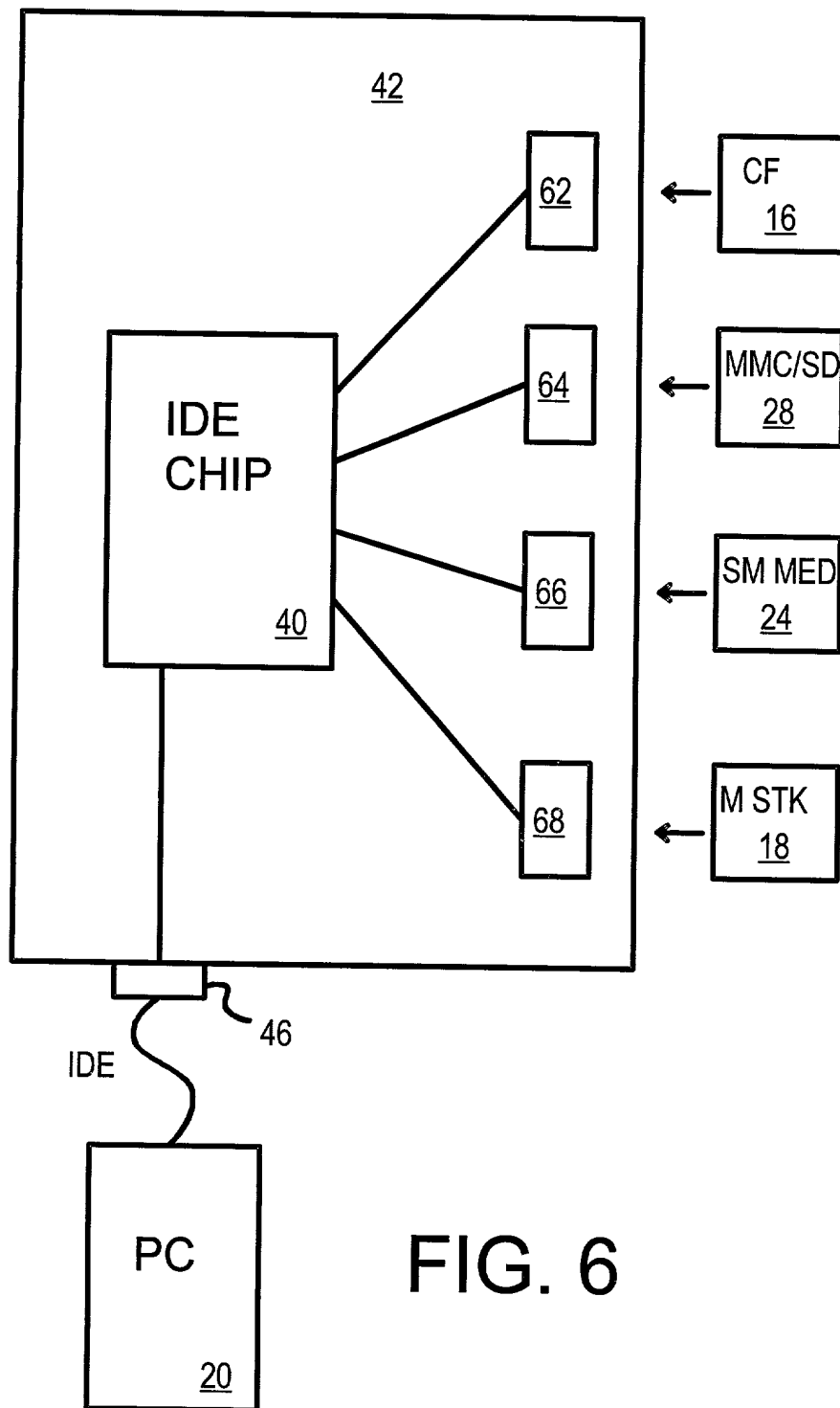


FIG. 6

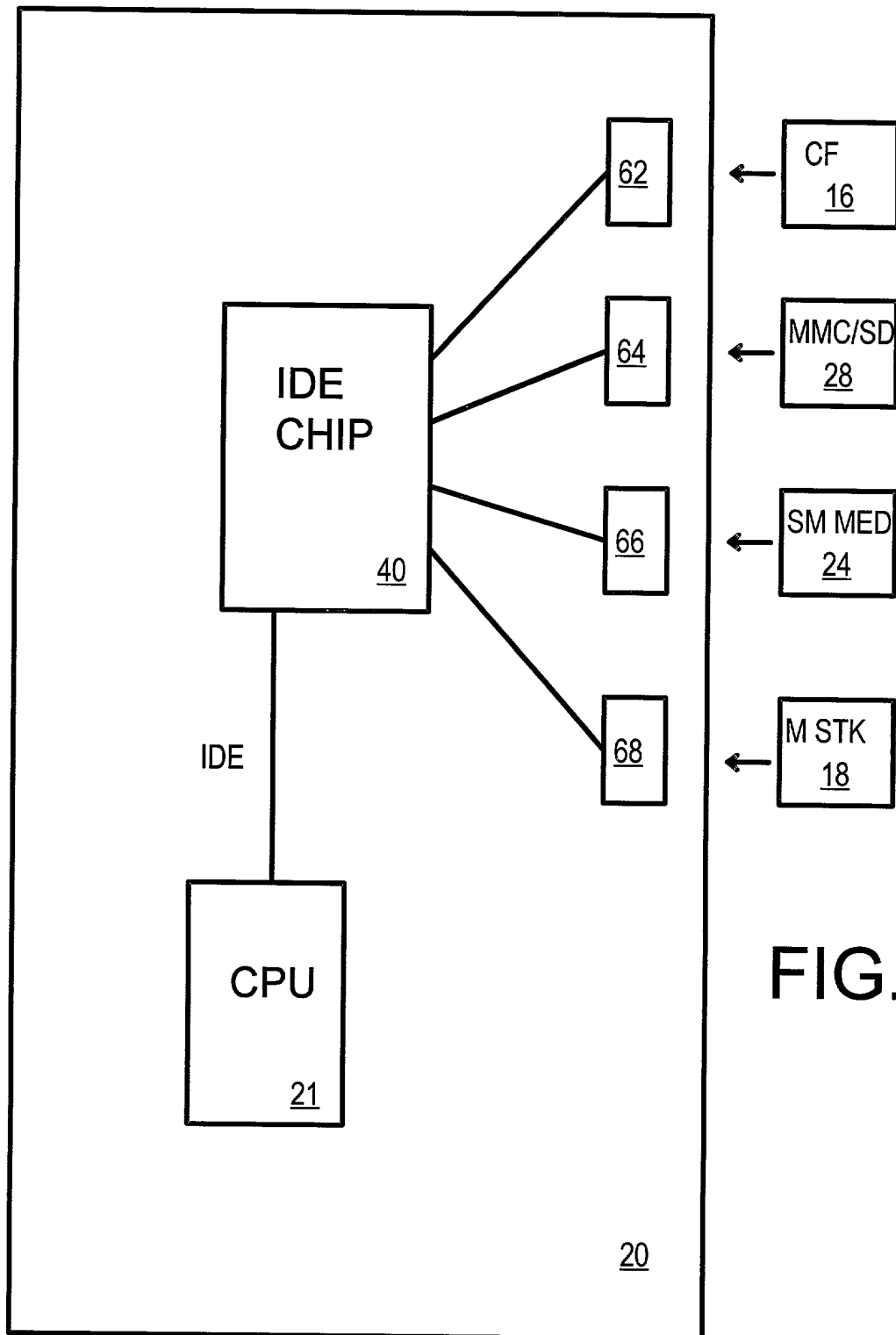


FIG. 7



FIG. 8

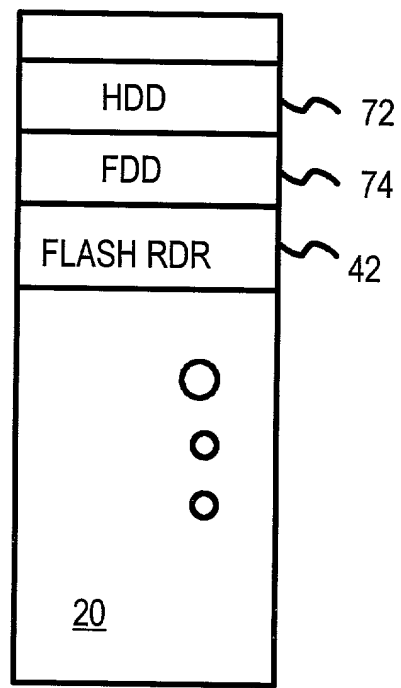


FIG. 8

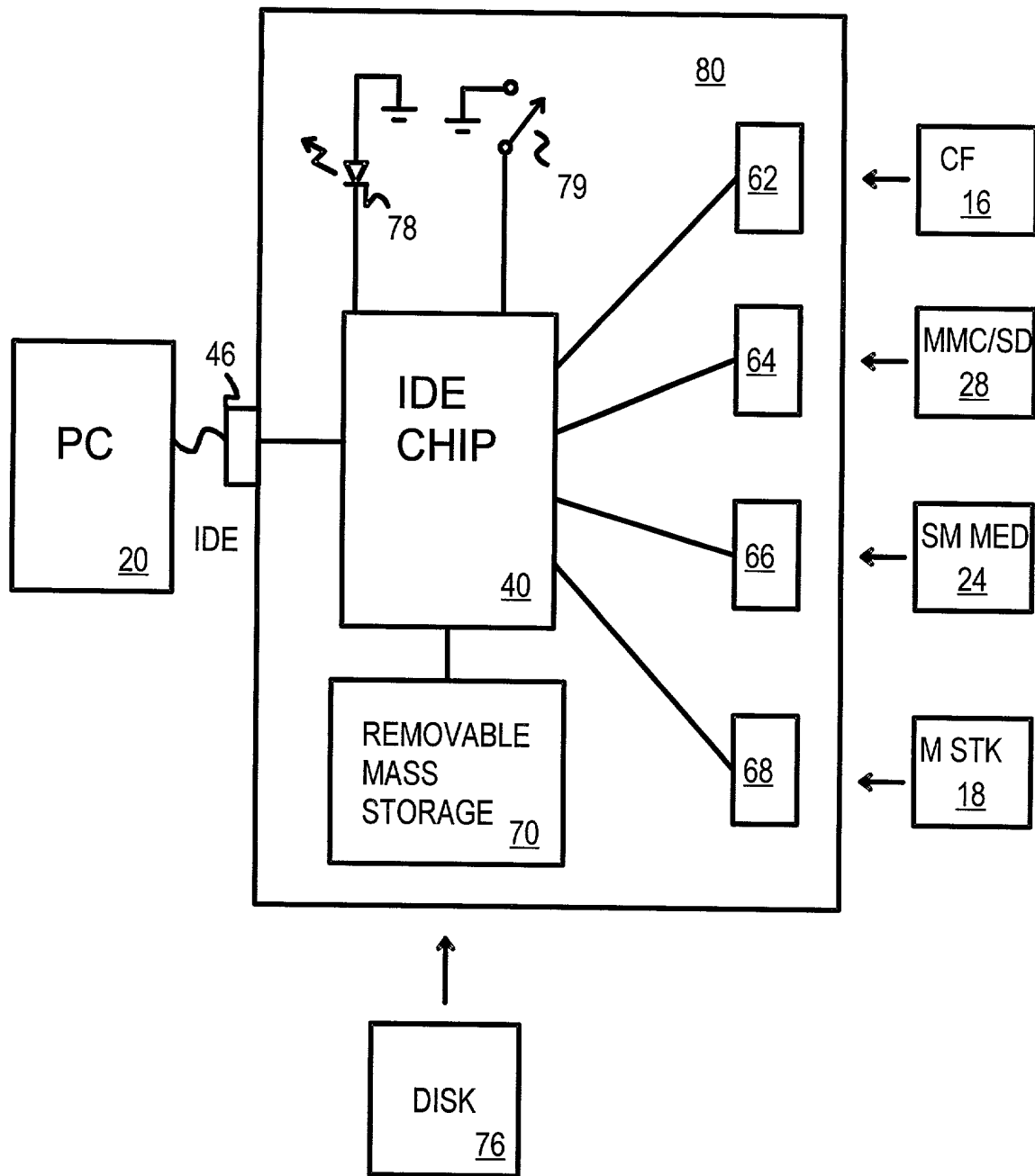


FIG. 9

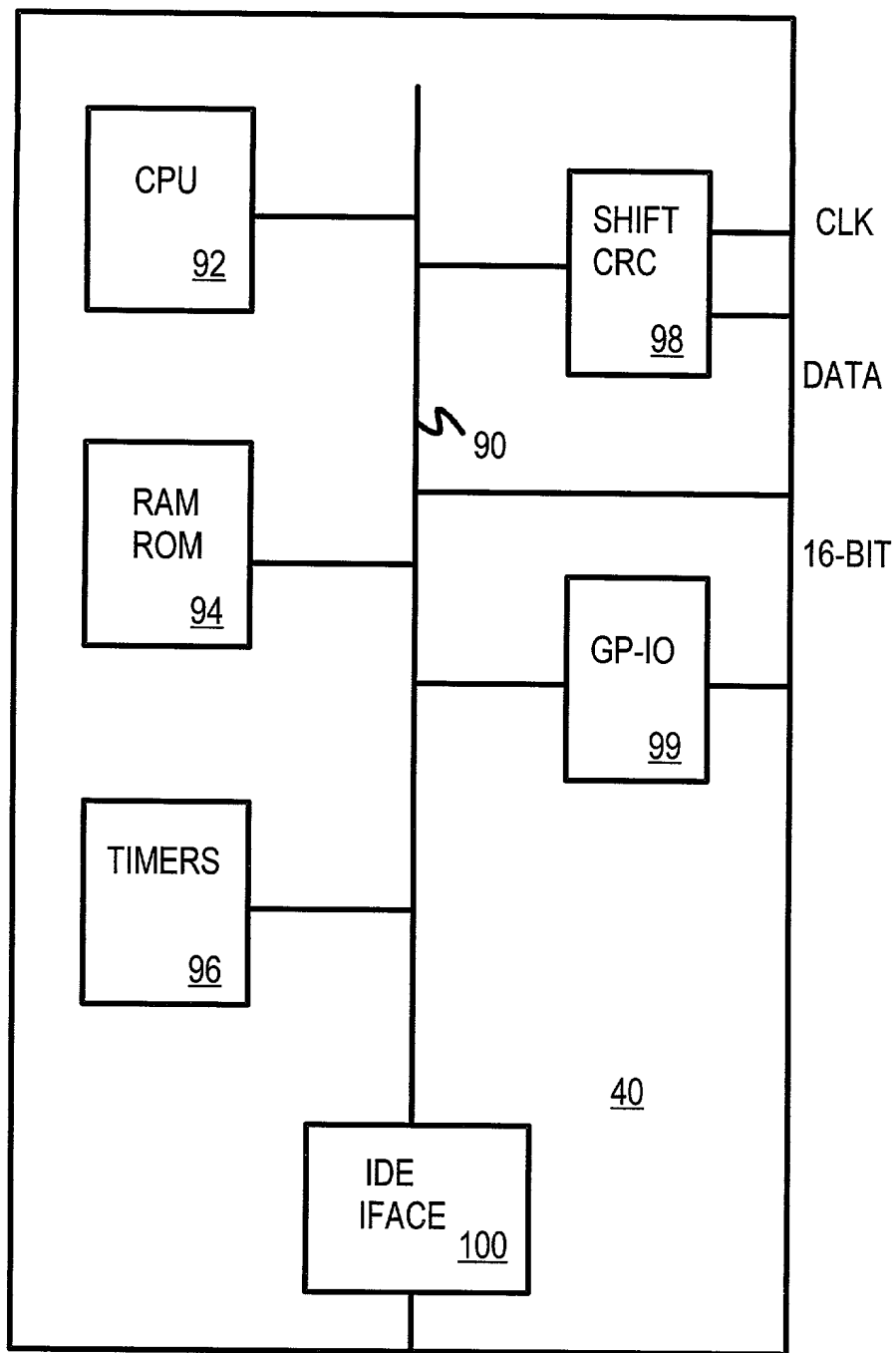


FIG. 10